

1 1. A method comprising:
2 delivering first data from a first position in a first serial buffer having a receiving
3 end to receive the first data and a delivering end opposite the receiving end; and
4 delivering second data from a second position in a second serial buffer having a
5 receiving end to receive the second data and a delivering end opposite the receiving end, and the
6 first position relative to the delivering end of the first serial buffer is different than the second
7 position relative to the delivering end of the second serial buffer.

1 2. The method of claim 1 including delivering the first data from the first position in
2 the first serial buffer having a number of elements that exceeds a maximum lane-to-lane skew.

1 3. The method of claim 2 including delivering the first data from the first position in
2 the first serial buffer having eight elements.

1 4. The method of claim 1 further including locating a first element in the first serial
2 buffer and thereafter allowing $X = B * E$ bits to proceed through the first serial buffer before
3 delivering the first data, wherein B equals a number of bits capable of being stored in an element,
4 and E equals a number of elements in the first serial buffer.

1 5. The method of claim 1 further including determining a location of a first element
2 in the first serial buffer, basing the first position on the location of the first element in the first
3 serial buffer, determining a location of a second element in the second serial buffer, and basing
4 the second position on the location of the second element in the second serial buffer.

1 6. The method of claim 1 further including locating a first element in the first serial
2 buffer, and locating a second element in the second serial buffer, independently.

1 7. An article comprising a medium storing instructions that, if executed, enable a
2 processor-based system to:
3 deliver first data from a first position in a first serial buffer having a receiving end
4 to receive the first data and a delivering end opposite the receiving end; and
5 deliver second data from a second position in a second serial buffer having a
6 receiving end to receive the second data and a delivering end opposite the receiving end, and the
7 first position relative to the delivering end of the first serial buffer is different than the second
8 position relative to the delivering end of the second serial buffer.

1 8. The article of claim 7 storing instructions that, if executed, enable a processor-
2 based system to deliver the first data from the first position in the first serial buffer having a
3 number of elements that exceeds a maximum lane-to-lane skew.

1 9. The article of claim 8 storing instructions that, if executed, enable a processor-
2 based system to deliver the first data from the first position in the first serial buffer having eight
3 elements.

1 10. The article of claim 7 further storing instructions that, if executed, enable a
2 processor-based system to locate a first element in the first serial buffer and thereafter allow $X =$
3 $B \cdot E$ bits to proceed through the first serial buffer before delivering the first data, wherein B
4 equals a number of bits capable of being stored in an element, and E equals a number of
5 elements in the first serial buffer.

1 11. The article of claim 7 further storing instructions that, if executed, enable a
2 processor-based system to:
3 determine a location of a first element in the first serial buffer; and

4 determine a location of a second element in the second serial buffer, and the first
5 position is based on the location of the first element in the first serial buffer, and the second
6 position is based on the location of the second element in the second serial buffer.

1 12. The article of claim 7 further storing instructions that, if executed, enable a
2 processor-based system to:

3 locate a first element in the first serial buffer; and
4 locate a second element in the second serial buffer, wherein locating the first
5 element and locating the second element are capable of being performed independently.

1 13. An apparatus comprising:
2 a first serial buffer to receive first data at a receiving end of the first serial buffer,
3 and the first serial buffer includes a delivering end, opposite the receiving end, and a first
4 position;
5 a second serial buffer to receive second data at a receiving end of the second
6 serial buffer, and the second serial buffer includes a delivering end, opposite the receiving end,
7 and a second position; and
8 a controller to determine that the first data is to be delivered from the first position
9 and that the second data is to be delivered from the second position, and the first position relative
10 to the delivering end of the first serial buffer is different than the second position relative to the
11 delivering end of the second serial buffer.

1 14. The apparatus of claim 13, wherein the first serial buffer includes a number of
2 elements, and the number of elements exceeds a maximum lane-to-lane skew.

1 15. The apparatus of claim 14, wherein the number of elements is eight.

1 16. The apparatus of claim 13 including the controller to locate a first element in the
2 first serial buffer and to allow $X = B * E$ bits to proceed through the first serial buffer after the
3 first element is located before delivering the first data, wherein B equals a number of bits capable
4 of being stored in an element, and E equals a number of elements in the first serial buffer.

1 17. The apparatus of claim 13 including the controller to determine a location of a
2 first element in the first serial buffer and to determine a location of a second element in the
3 second serial buffer, and the first position is based on the location of the first element in the first
4 serial buffer, and the second position is based on the location of the second element in the second
5 serial buffer.

1 18. The apparatus of claim 13 including the controller to locate a first element in the
2 first serial buffer and to locate a second element in the second serial buffer, and locating the first
3 element and locating the second element are capable of being performed independently.

1 19. The apparatus of claim 13, wherein the apparatus is an Ethernet controller.

1 20. The apparatus of claim 13, wherein the apparatus is an InfiniBand™ adaptor.

1 21. A system comprising:
2 a processor-based device;
3 a bridge to direct data received from the processor-based device; and
4 a storage coupled to the processor-based device storing instructions that, if
5 executed, enable the processor-based device to:
6 deliver first data from a first position in a first serial buffer having a
7 receiving end to receive the first data and a delivering end opposite the receiving end, and
8 deliver second data from a second position in a second serial buffer having
9 a receiving end to receive the second data and a delivering end opposite the receiving end, and

10 the first position relative to the delivering end of the first serial buffer is different than the second
11 position relative to the delivering end of the second serial buffer.

1 22. The system of claim 21 storing instructions that, if executed, enable the processor-
2 based device to deliver the first data from the first position in the first serial buffer having a
3 number of elements that exceeds a maximum lane-to-lane skew.

1 23. The system of claim 22 storing instructions that, if executed, enable the processor-
2 based device to deliver the first data from the first position in the first serial buffer having eight
3 elements.

1 24. The system of claim 21 further storing instructions that, if executed, enable the
2 processor-based device to locate a first element in the first serial buffer and thereafter allow $X =$
3 $B * E$ bits to proceed through the first serial buffer before delivering the first data, wherein B
4 equals a number of bits capable of being stored in an element, and E equals a number of
5 elements in the first serial buffer.

1 25. The system of claim 21 further storing instructions that, if executed, enable the
2 processor-based device to:
3 determine a location of a first element in the first serial buffer; and
4 determine a location of a second element in the second serial buffer, and the first
5 position is based on the location of the first element in the first serial buffer, and the second
6 position is based on the location of the second element in the second serial buffer.

1 26. The system of claim 21 further storing instructions that, if executed, enable the
2 processor-based device to:
3 locate a first element in the first serial buffer; and

- 4 locate a second element in the second serial buffer, wherein locating the first
- 5 element and locating the second element are capable of being performed independently.